



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Daniel J. Smith et al.	)	Examiner: Siek, Vuthe
	)	
Serial No.: 10/627,056	)	Art Unit: 2825
	)	
Filed: July 25, 2003	)	Confirmation No. 2207
	)	
For: GENERATING A SPLIT POWER PLANE OF A	)	Attorney Docket No. 003921.00151
MULTI-LAYER PRINTED CIRCUIT BOARD	)	

**INFORMATION DISCLOSURE STATEMENT**

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Sir:

Pursuant to 37 C.F.R. § 1.56, the attention of the U.S. Patent and Trademark Office is hereby directed to the references listed in the attached Form PTO/SB/08A. A copy of the listed references is enclosed.

Applicants submit this information so that the Patent and Trademark Office may determine any materiality thereof to the claimed invention. It is respectfully requested that the information be formally considered during the prosecution of this application, and that the listed references appear among the "References Cited" on any patent to issue herefrom. The

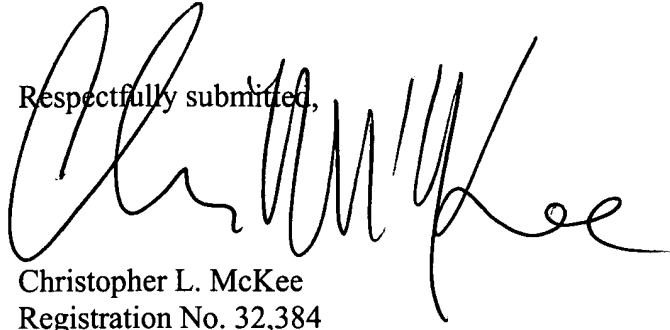
04/26/2005 JADD01 00000051 190733 10627056  
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Serial Number: 10/627,056

Attorney Docket No.: 003921.00151

Commissioner is authorized to charge the fee (\$180.00) required under 37 C.F.R. §1.17(p) to our deposit account number 19-0733.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Christopher L. McKee". The signature is fluid and cursive, with the first name "Chris" and last name "McKee" clearly distinguishable.

Christopher L. McKee  
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Date: April 25, 2005

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Substitute for form 1449B/PTO

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1 of 1

### Complete if Known

Application Number	10/627,056
Filing Date	July 25, 2003
First Named Inventor	Daniel J. Smith
Art Unit	2825
Examiner Name	Siek, Vuthe
Attorney Docket Number	003921.00151

### NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		T. HUBING et al., Preliminary Embedded Capacitance Measurement Results, NCMS Embedded Capacitance Project, 10/23/99, 25 pp.	
		TODD HUBING et al., Power Bus Noise Reduction Using Power Islands in Printed Circuit Board Designs, University of Missouri-Rolla Electromagnetic Compatibility Laboratory, November 11, 1999, 11 pp.	
		TODD HUBING, Prof., University of Missouri-Rolla, PWB Power Bus Structures: Theory and Design, University of Missouri-Rolla Electromagnetic Compatibility Laboratory, IEEE EMC Society Rocky Mountain Chapter, November 11, 1999, 24 pp.	
		ISTVAN NOVAK et al., SUN Microsystems, Inc., Simulating Complex Power-Ground Plane Shapes With Variable-Size Cell SPICE Grids, Manuscript for the 11 <sup>th</sup> Topical Meeting on Electrical Performance of Electronic Packaging October 21-23, 2002, Monterey, CA	

Examiner  
Signature

Date  
Considered

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 120 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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